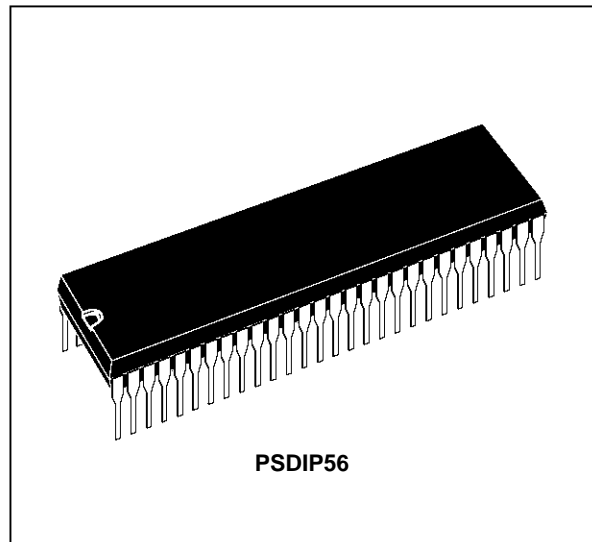# SGS-THOMSON MICROELECTRONICS

ST7271

8-BIT HCMOS MCUs WITH AND TV/MONITOR DEDICATED FUNCTIONS

PRODUCT OVERVIEW

- 5V ± 10% supply operating range
- 4MHz Maximum Internal Clock Frequency
- Fully static operation
- 0 to +70°C Operating Temperature Range
- Run, Wait, and Stop Modes
- User ROM: up to   15040 bytes
  Data RAM: up to   256 bytes
  EEPROM: up to   768 bytes
  (including EWPCC EEPROM: 256 bytes)
- 56 pin Shrink Dual In Line Package (ST7271N)
- up to 27 I/O lines
  8 I/O Open Drain with 12V capability
- up to 8 lines programmable as interrupt wake-up inputs
- 16-bit timer with 2 input capture and 2 output compare functions
- Sync Processor for video timing analysis
- East/West Pin Cushion Automatic Correction with DAC output.
- Watchdog for system reliability and integrity
- 8-bit Analog to Digital Converter with up to 8 channels
- 16 10-bit PWM/BRM Digital to Analog outputs
- 2 12-bit PWM/BRM Digital to Analog outputs
- Industry Standard Serial Peripheral Interface
- User mask options:
  - SPI Data Rate
  - Watchdog enable/disable after Reset
  - Watchdog enable during WAIT mode
- Master Reset and Power-on/off reset
- Full Hardware Emulator
- 8-bit data manipulation
- 74 basic instructions
- 10 main addressing modes
- 8x8 unsigned multiply instruction
- true bit manipulation
- Complete development support on Real-time emulator with PC/DOS
- Full software package (Cross Assembler, debugger)

PSDIP56

Datasheet not available, please contact your ST sales office for further information

DEVICE SUMMARY

DEVICE	ROM (Bytes)	RAM (Bytes)	EEPROM (Bytes)	PACKAGE
ST7271N5	15K	256	512	PSDIP56
ST7271N3	12K	256	512	PSDIP56
ST7271N2	8K	256	512	PSDIP56
ST7271N1	8K	192	384	PSDIP56